WHAT IS CLAIMED IS:

1. A method for determining a read strobe pulse delay for data read from a memory having a plurality of memory chips, each one of the chips providing data along with an associated read strobe pulse, the data read from each one of the plurality of chips being stored in a corresponding one of a plurality of storage devices in response to the read strobe pulse associated with such one of the plurality of chips, comprising:

(a) storing data written into the memory from the plurality of resources having a predetermined address into both the memory and into a local memory device;

(b) monitoring requests for the memory from a plurality of resources and from a strobe delay training system;

(c) granting the strobe delay training system access to the memory in response to a request from such training system;

(e) operating the access granted strobe delay training system to determine the read strobe pulse delay; and

(d) subsequently writing the data stored in the local memory device back into the memory at the predetermined location.

2. The method recited in claim 1 wherein the training system operation comprises:

(a) operating the training system when such system has been granted access to the memory to determine the strobe pulse delay, such read strobe pulse delay being when the data from the plurality of memory chips and stored in the plurality of storage devices in response to the read strobe pulses is valid; and

(b) using the determined read strobe pulse delay for read subsequently from the memory for use by the plurality of resources.

3. The method recited in claim 1 wherein the training system operation comprises:

(a) storing data written into the memory from the plurality of resources having a predetermined address into both the memory and into a local memory device;

(b) monitoring requests for the memory from a plurality of resources and from a strobe delay training system;

(c) granting the strobe delay training system access to the memory in response to a request from such training system;

(e) operating the access granted strobe delay training system to determine the read strobe pulse delay; and

(d) subsequently writing the data stored in the local memory device back into the memory at the predetermined location.